

EAST - [09756084 wcp.1] File View Edit Tools Window Help

Drafts
 Pending
 Active
 L17: (2) ("4721905" "5245231").pn.
 L18: (6) ("3588707" | "4675612" | "4713621" | "4845390" | "486851
 L19: (18) 5245231.URPN.
 L20: (6) 4721905.URPN.
 L21: (4) ("3984771" | "4025848" | "4144572" | "4471299").PN.
 L23: (4909) (375/354 or 375/371 or 375/377 or 375/375 or 327/141
 L24: (160799) (time or phase) near1 (difference or error or detected
 L25: (2012) 24 and 23
 L26: (823) 25 and (delay).clm.
 L27: (616) 25 and (stor\$4 or memory).clm.
 L28: (616) 25 and (stor\$4 or memory).clm.
 L29: (255) 26 and 28
 L22: (34) 18 19 20 21
 L32: (25) ("4414637" | "4482826" | "4636734" | "4777385" | "48810
 Failed
 Saved
 Favorites
 Tagged (2)
 UDC
 Queue
 Trash

Search... Edit... Preferences... Help... Clear...
 DBs USPAT
Default operator: OR
 Highlight all in term initially

	U	I	Document I	Issue Da	Page	Title	Current O	Current X	Retrieval	Inventor	S	C
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6509771	20030121	9	Enhanced operational	327/175	327/99		Atallah, Francois L.	<input checked="" type="checkbox"/>	<input type="checkbox"/>
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6222408	20010424	26	Synchronous delay circuit	327/271	327/277		Saeki, Takanori	<input checked="" type="checkbox"/>	<input type="checkbox"/>

Ready